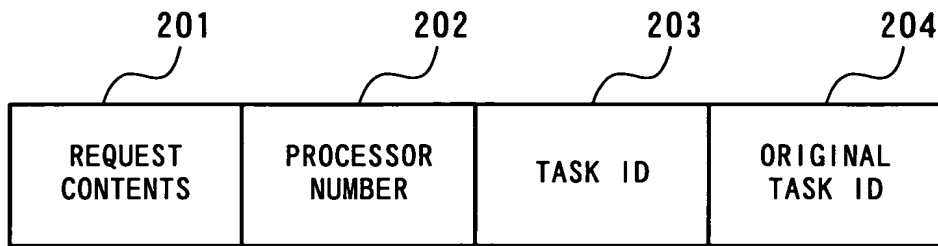


FIG. 3

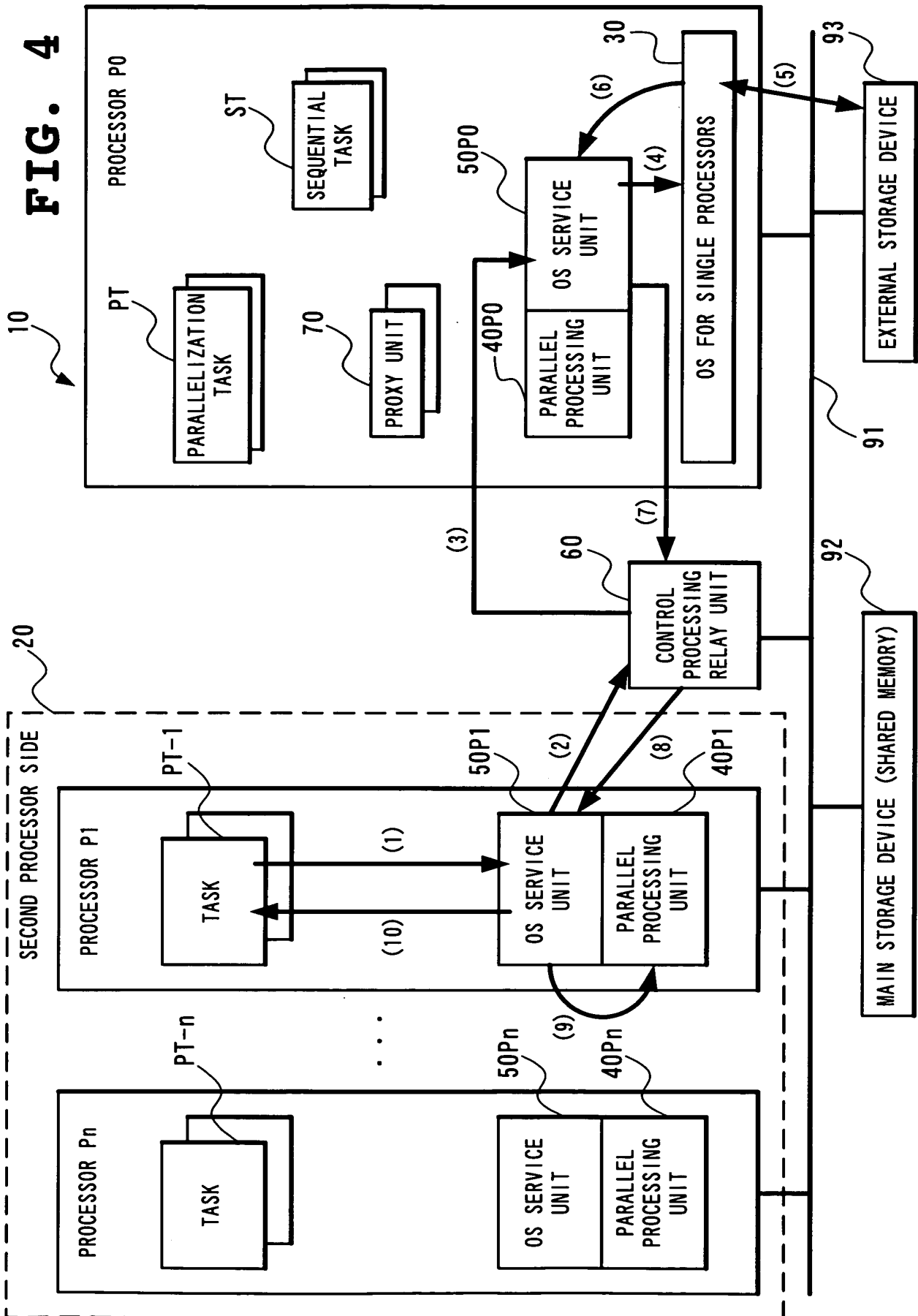
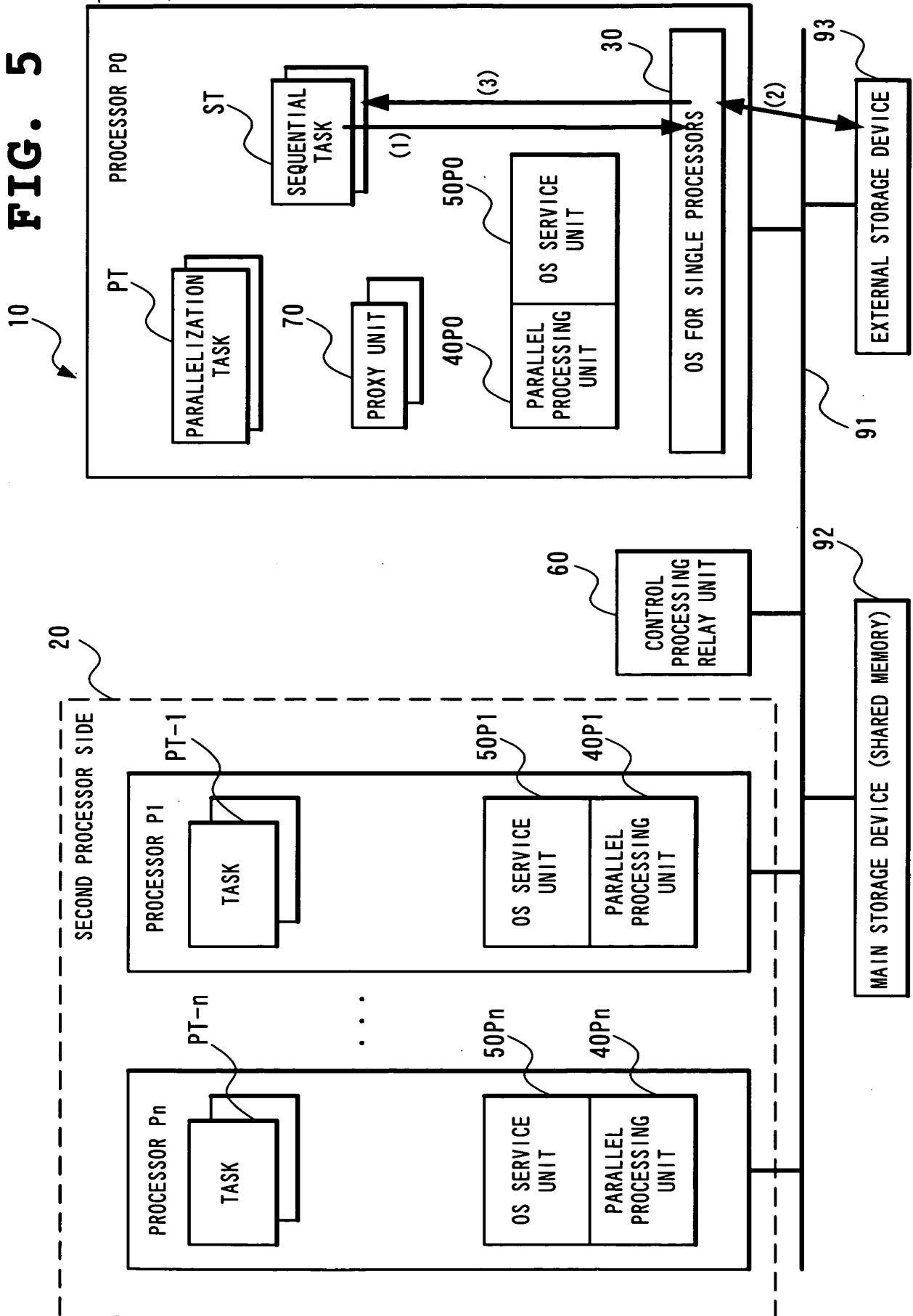
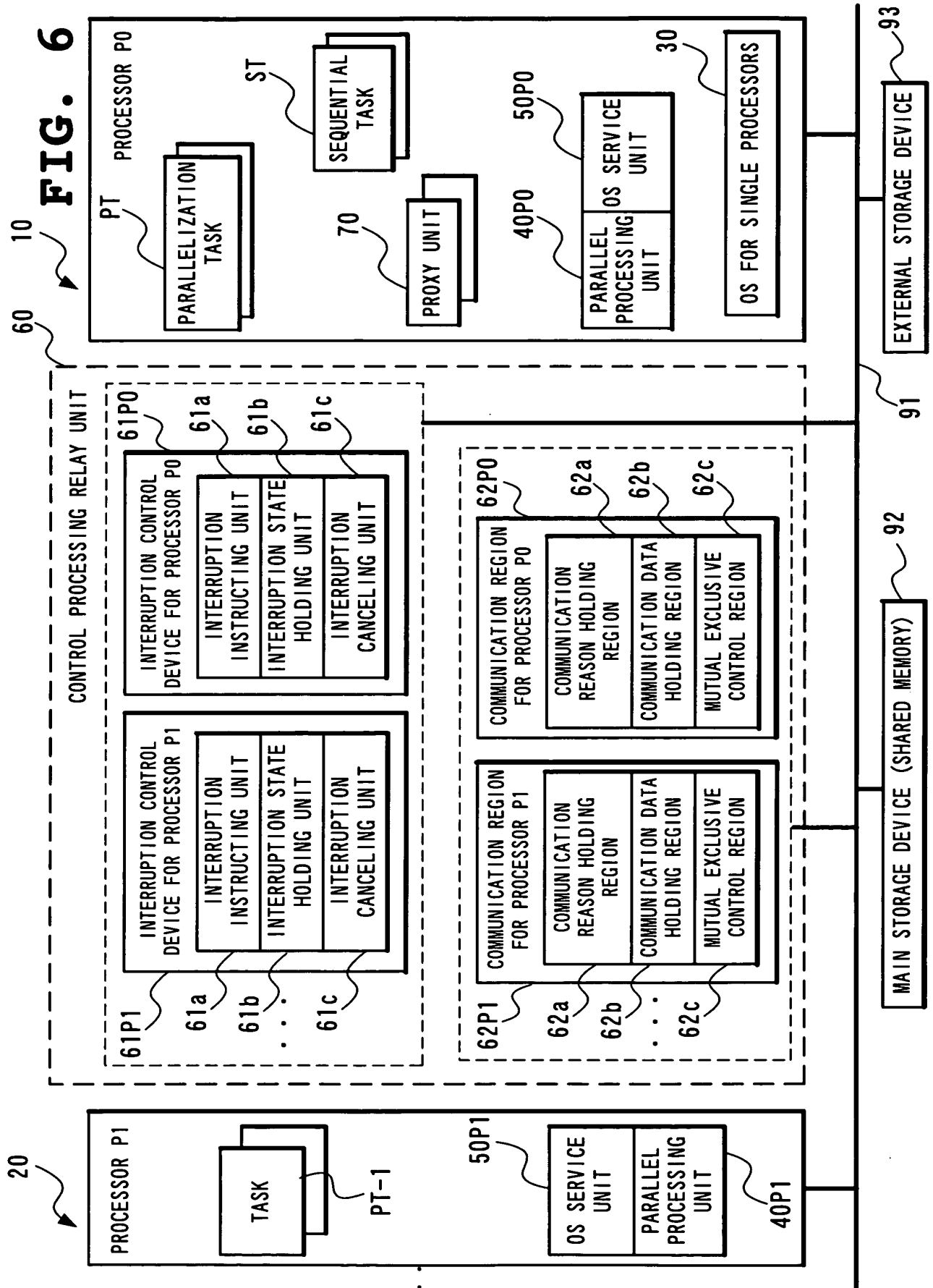


FIG. 5



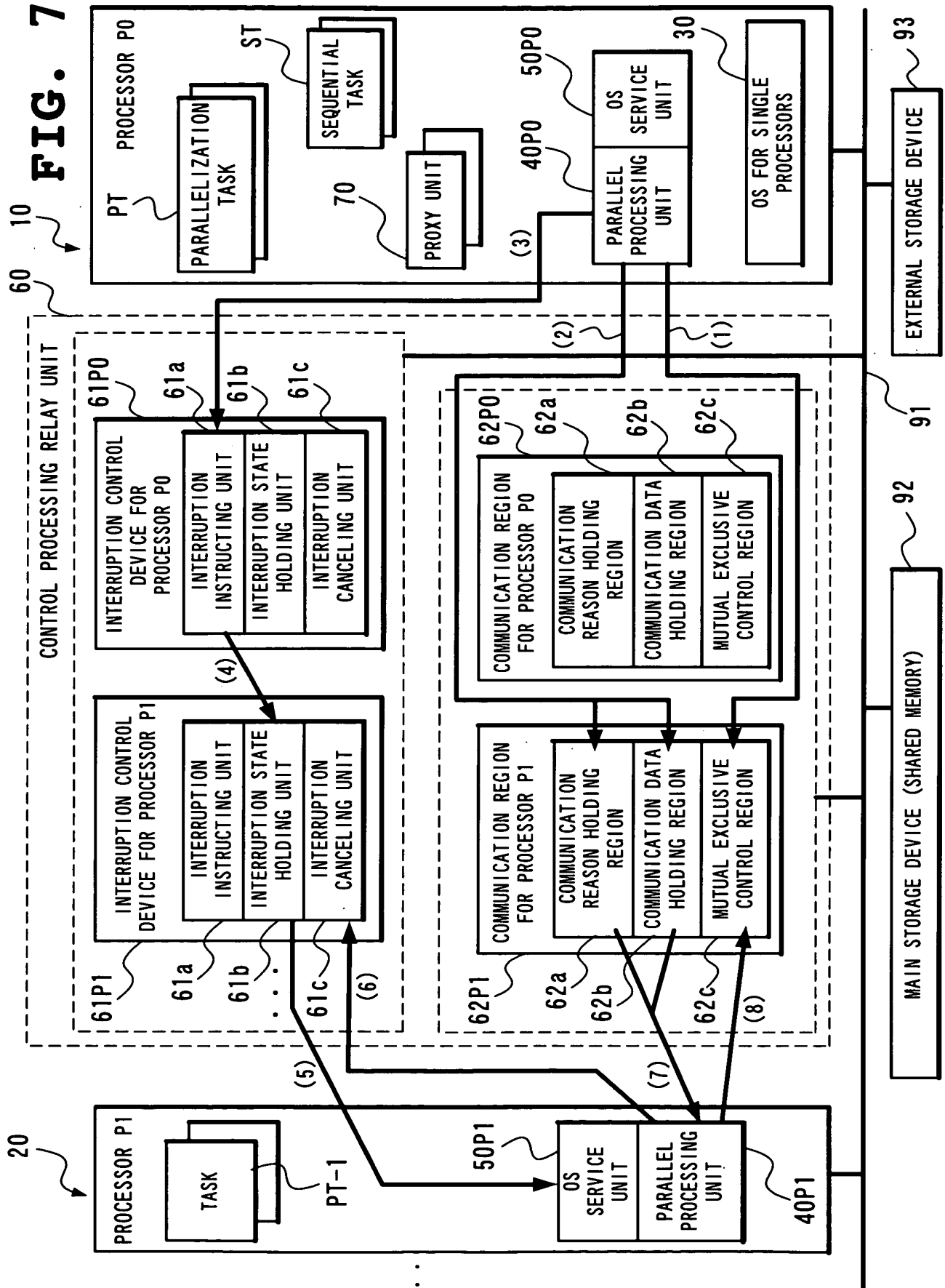


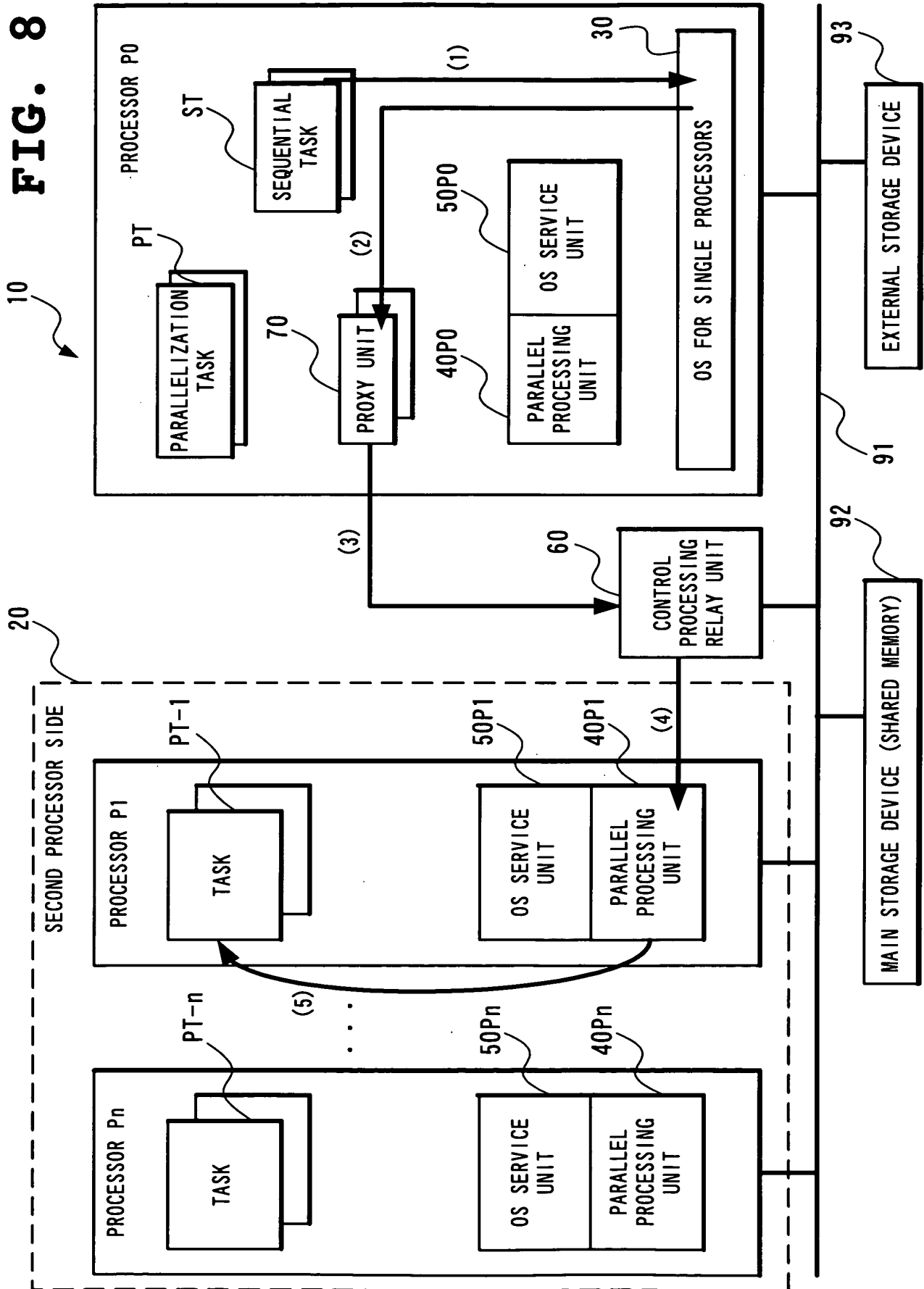
FIG. 8

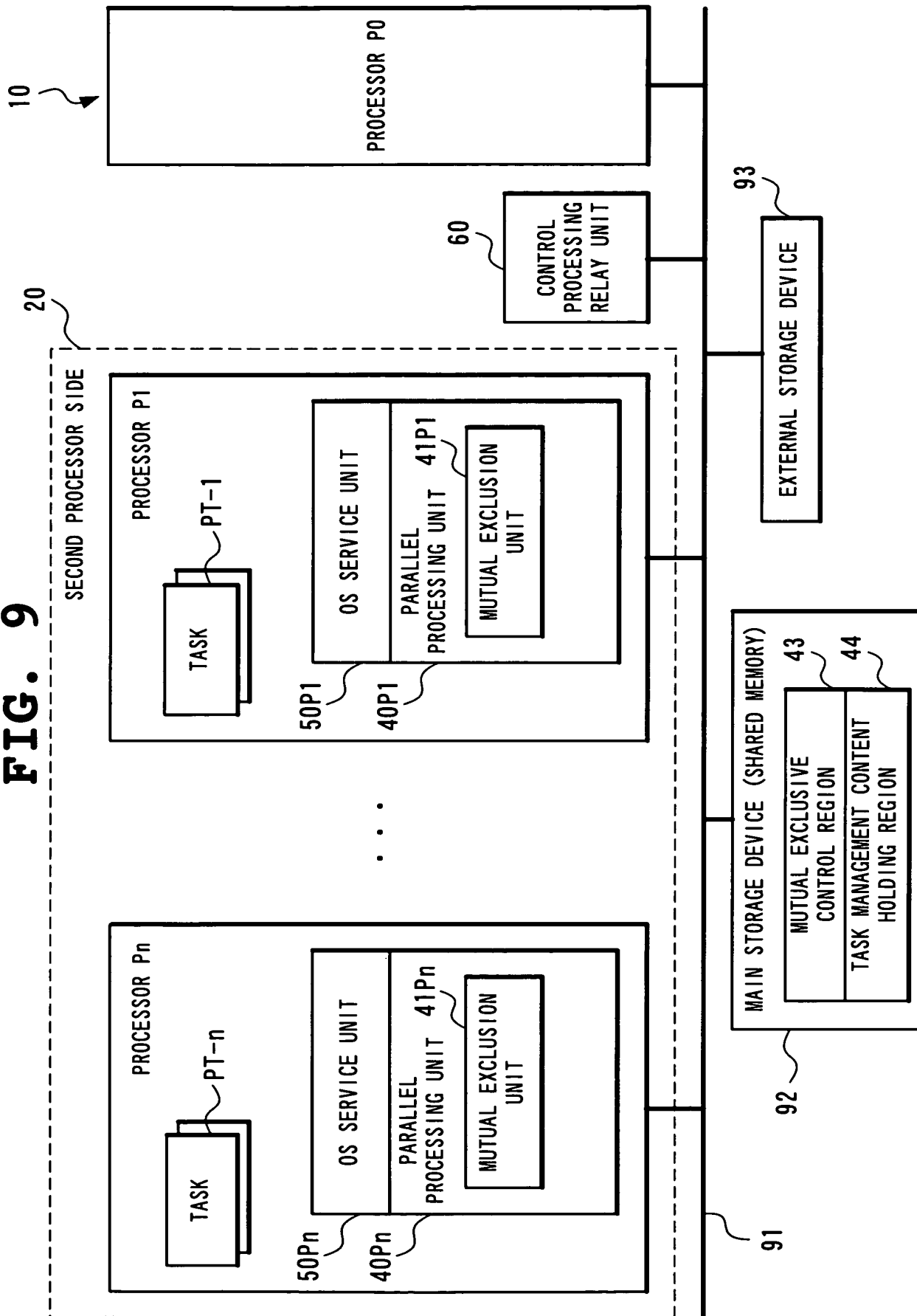
FIG. 9

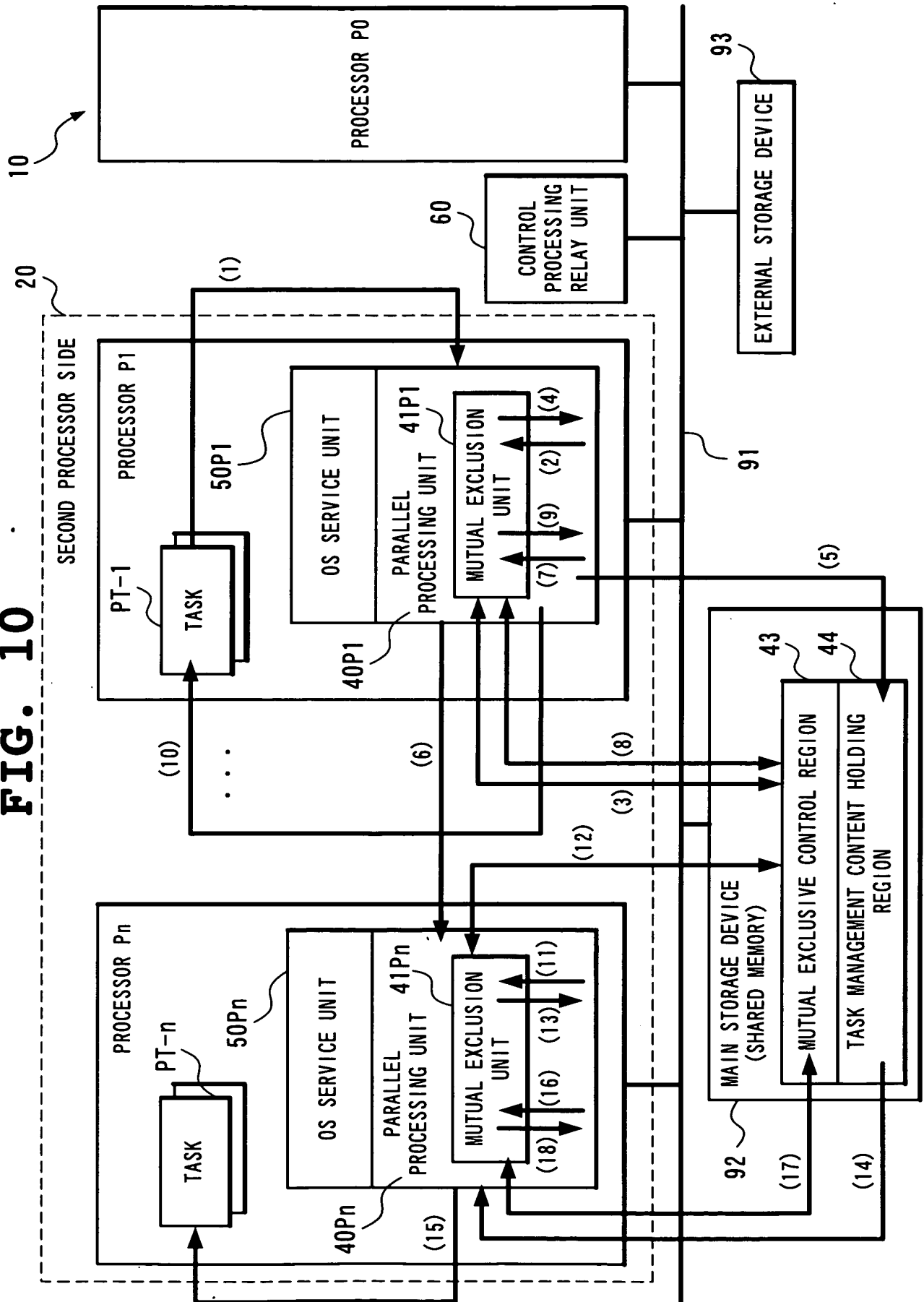
FIG. 10

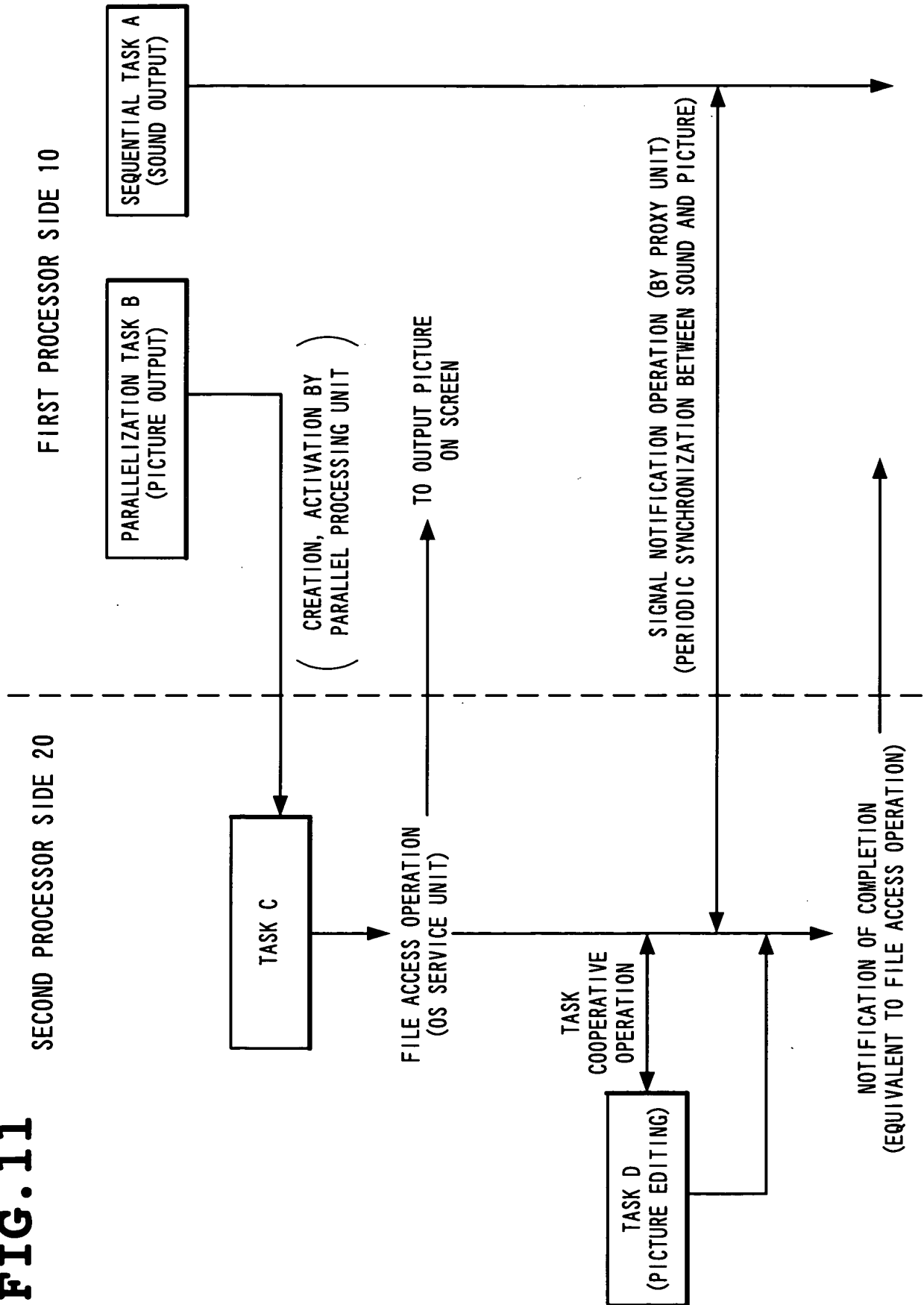
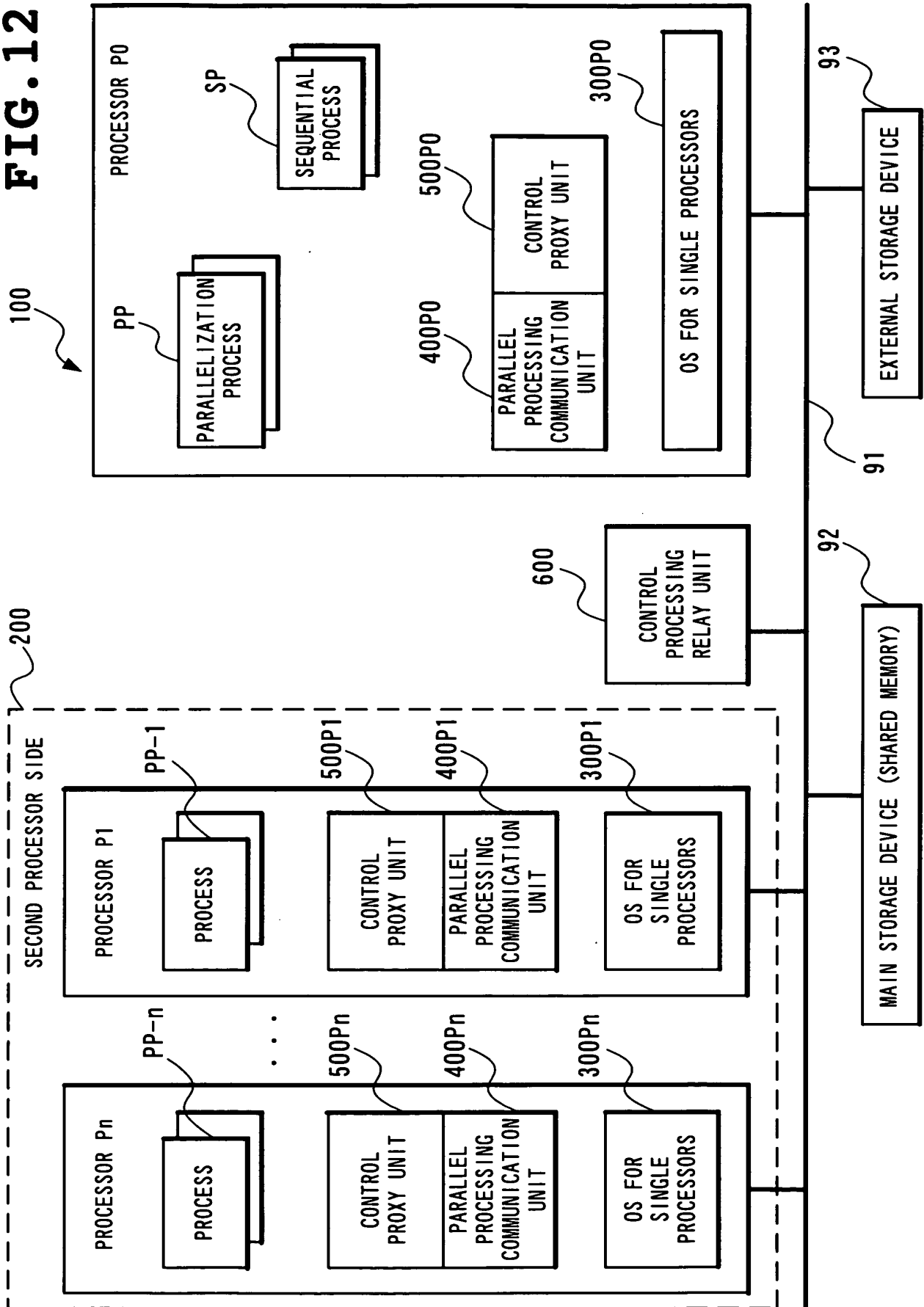
FIG. 11

FIG. 12

100

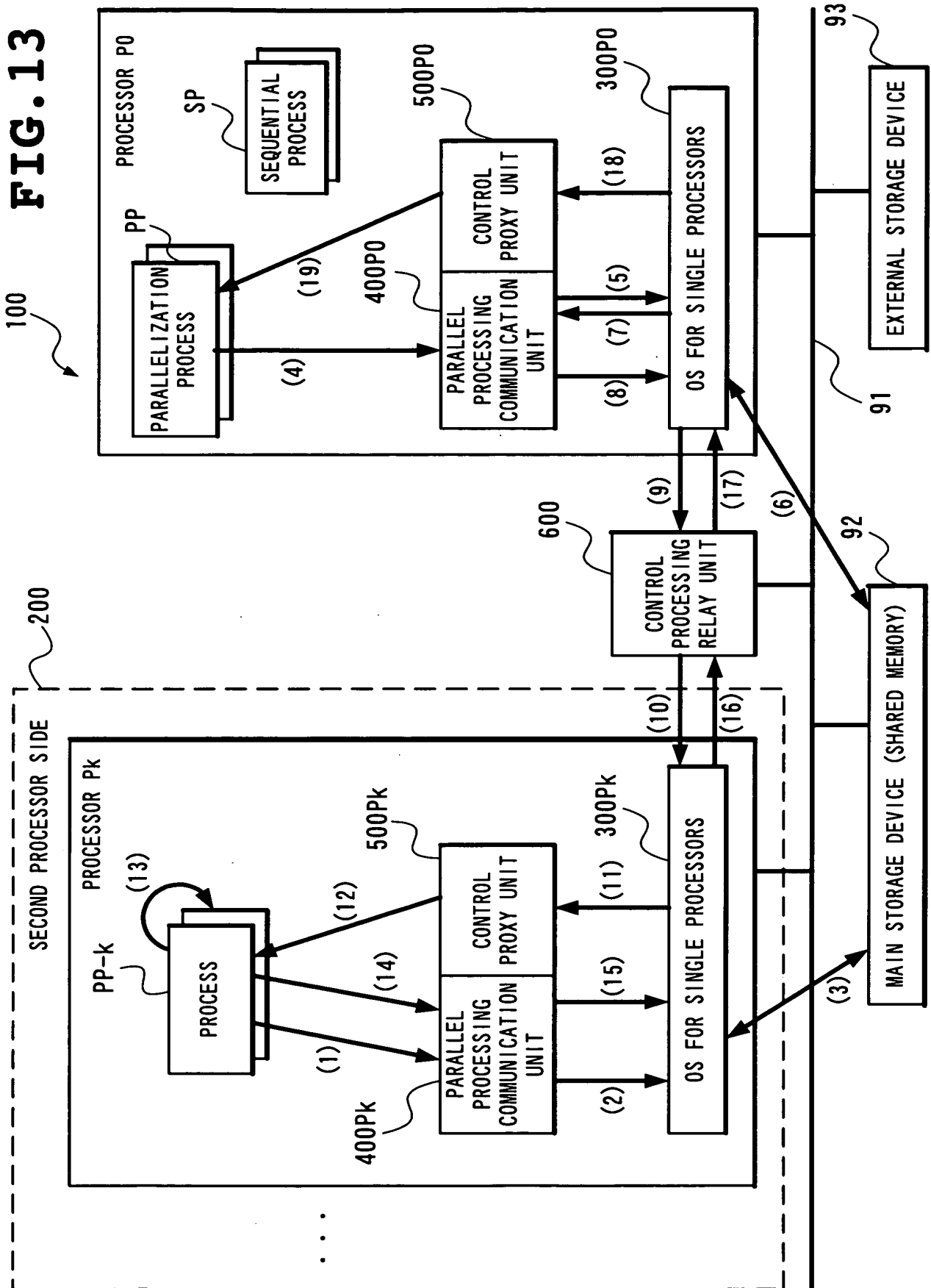
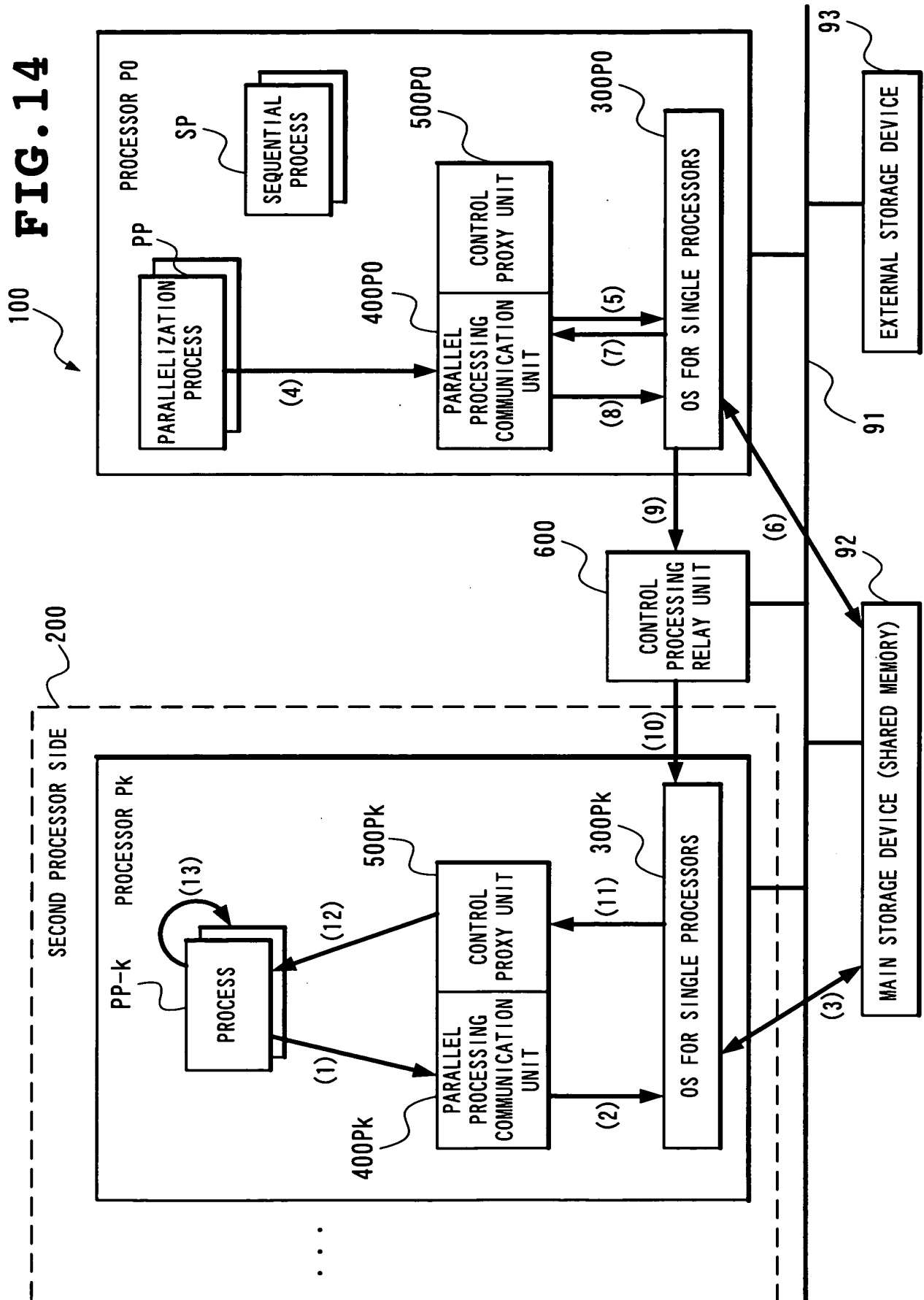


FIG. 14

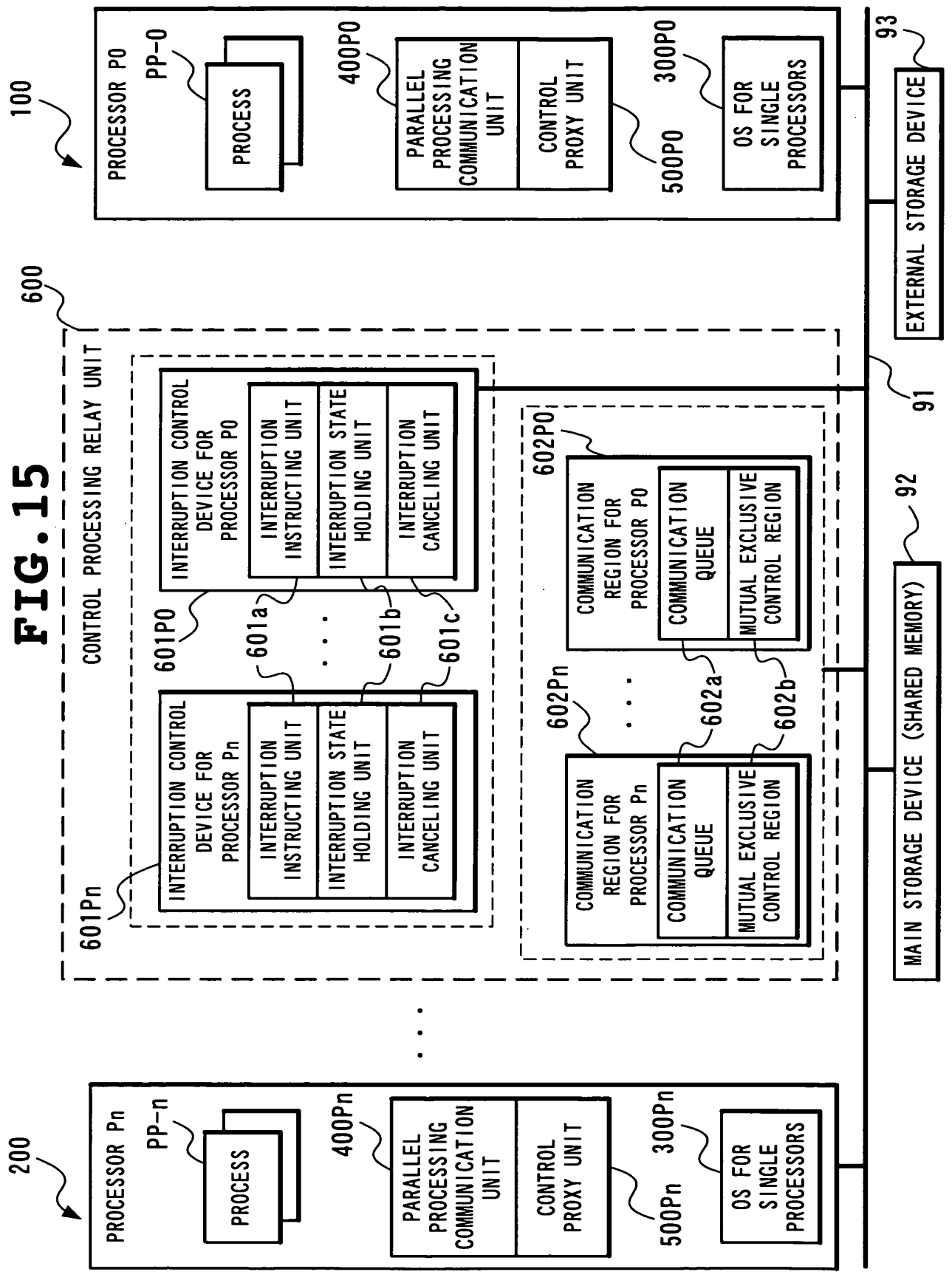


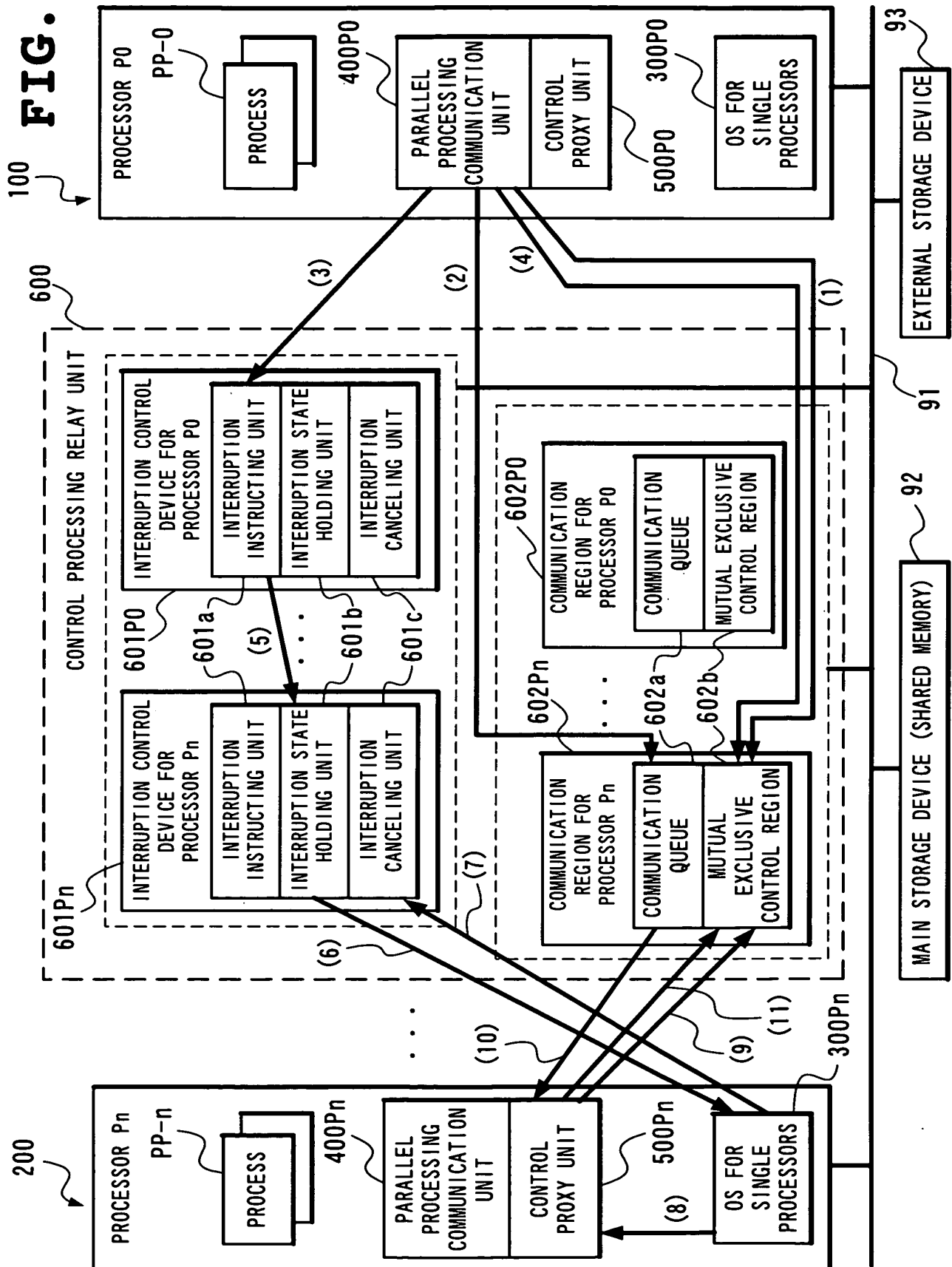
FIG. 16

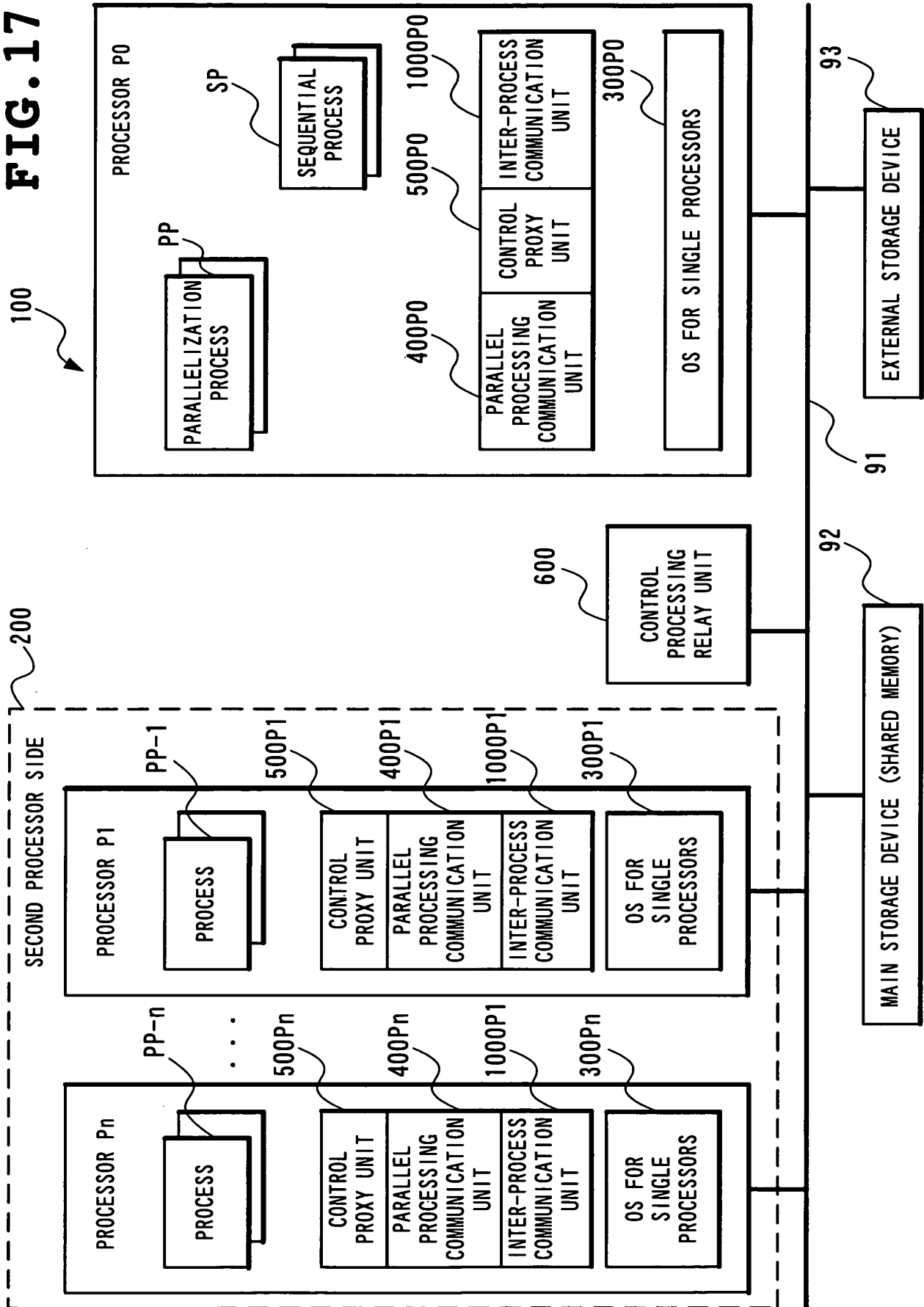
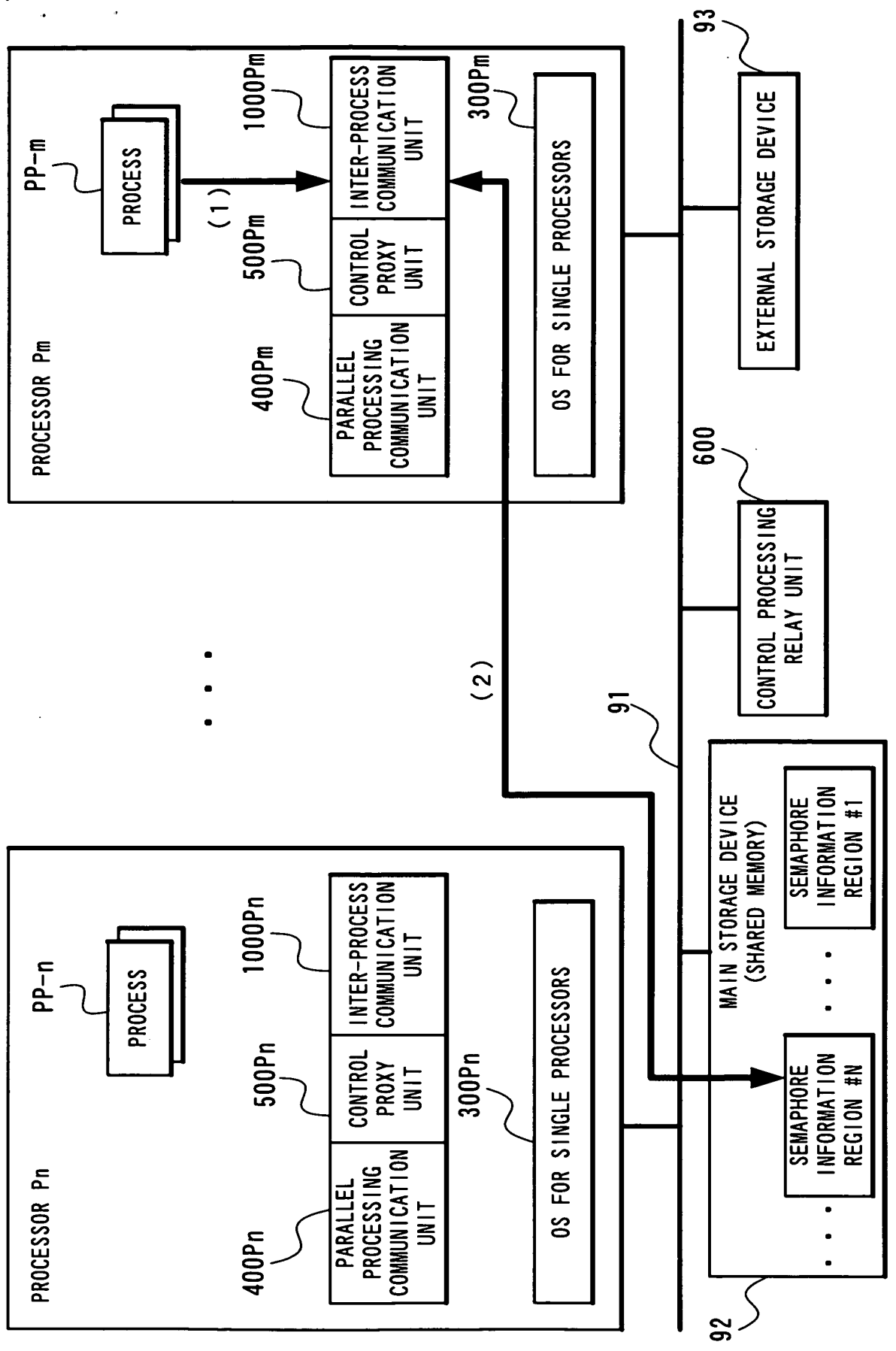
FIG. 17

FIG. 18 (UP/DOWN OPERATION BY SEMAPHORE SYSTEM)



(DOWN OPERATION BY SEMAPHORE)

FIG. 19

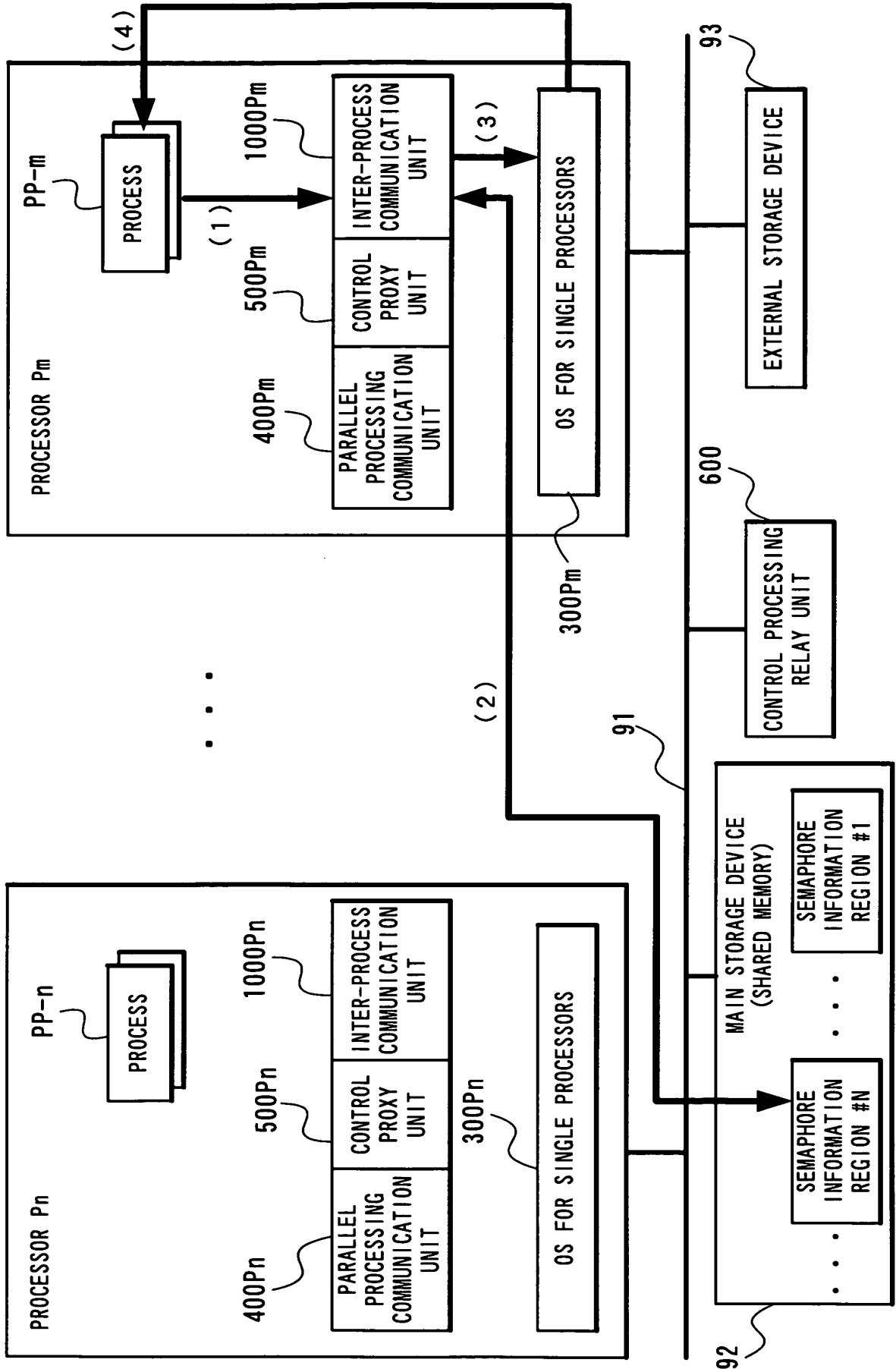


FIG. 20 (UP OPERATION BY SEMAPHORE SYSTEM ON THE SAME PROCESSOR)

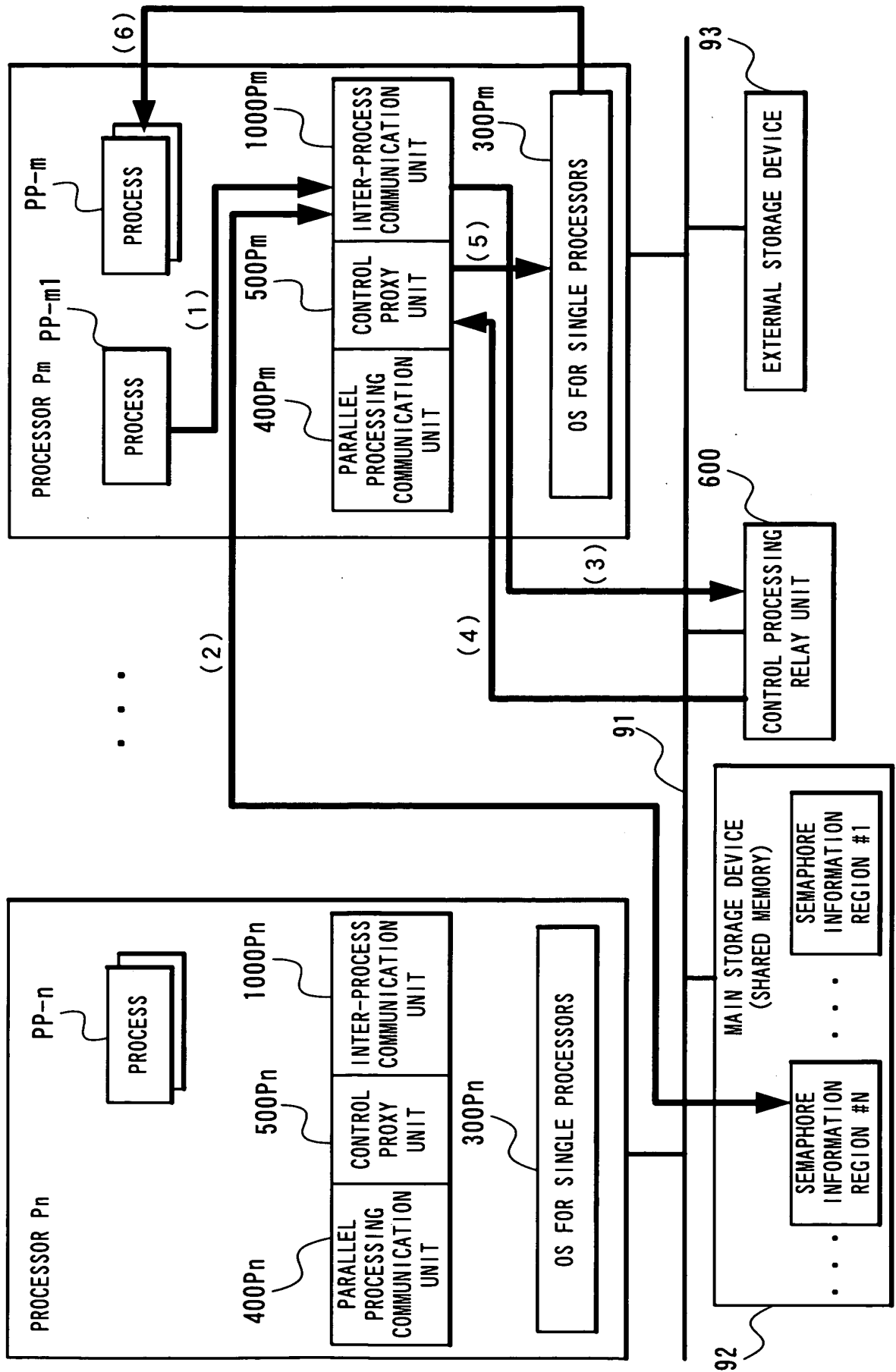


FIG. 21 (UP OPERATION BY SEMAPHORE SYSTEM BETWEEN DIFFERENT PROCESSORS)

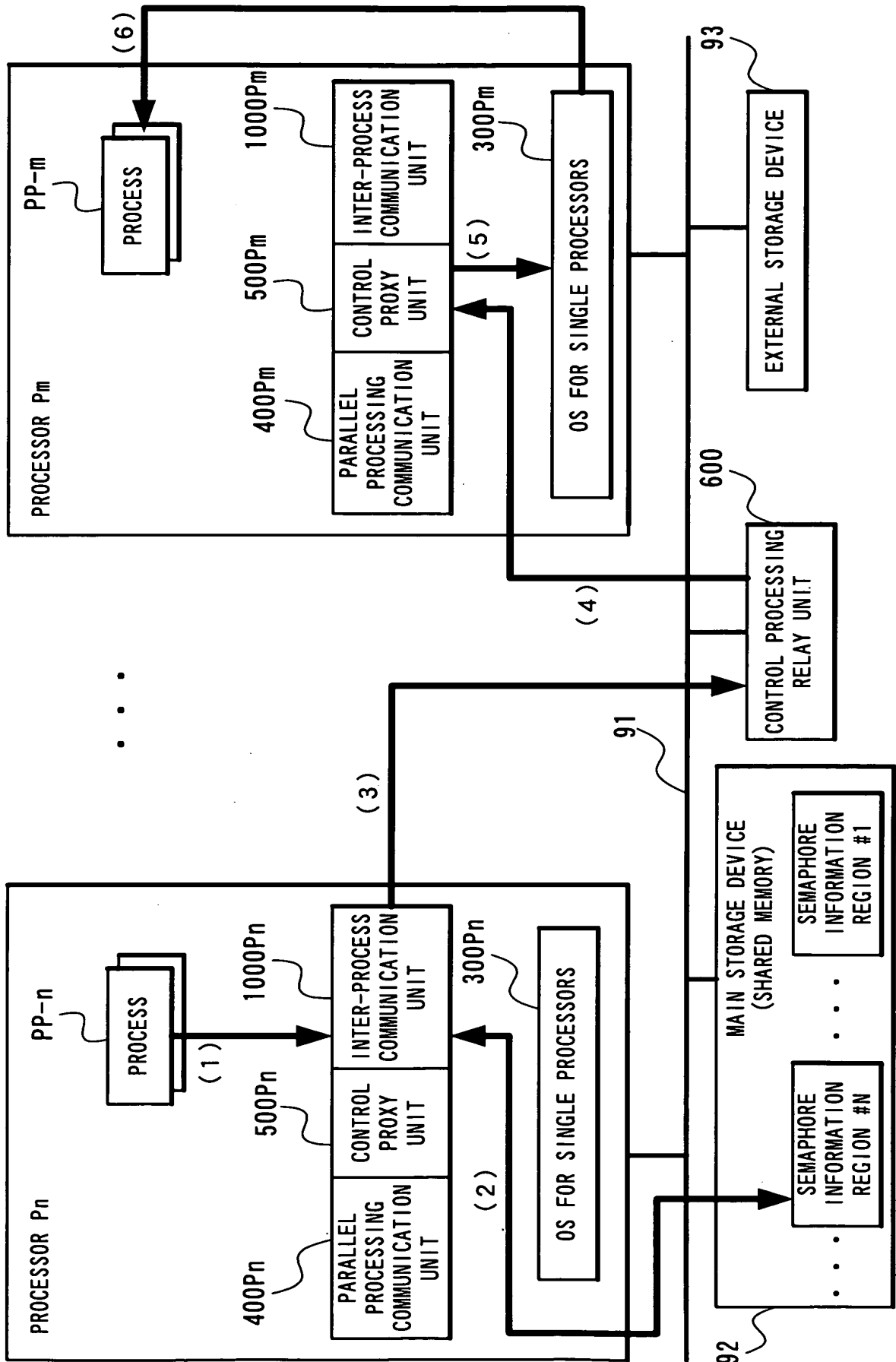


FIG. 22 (MESSAGE RECEPTION OPERATION BY MESSAGE QUEUE SYSTEM)

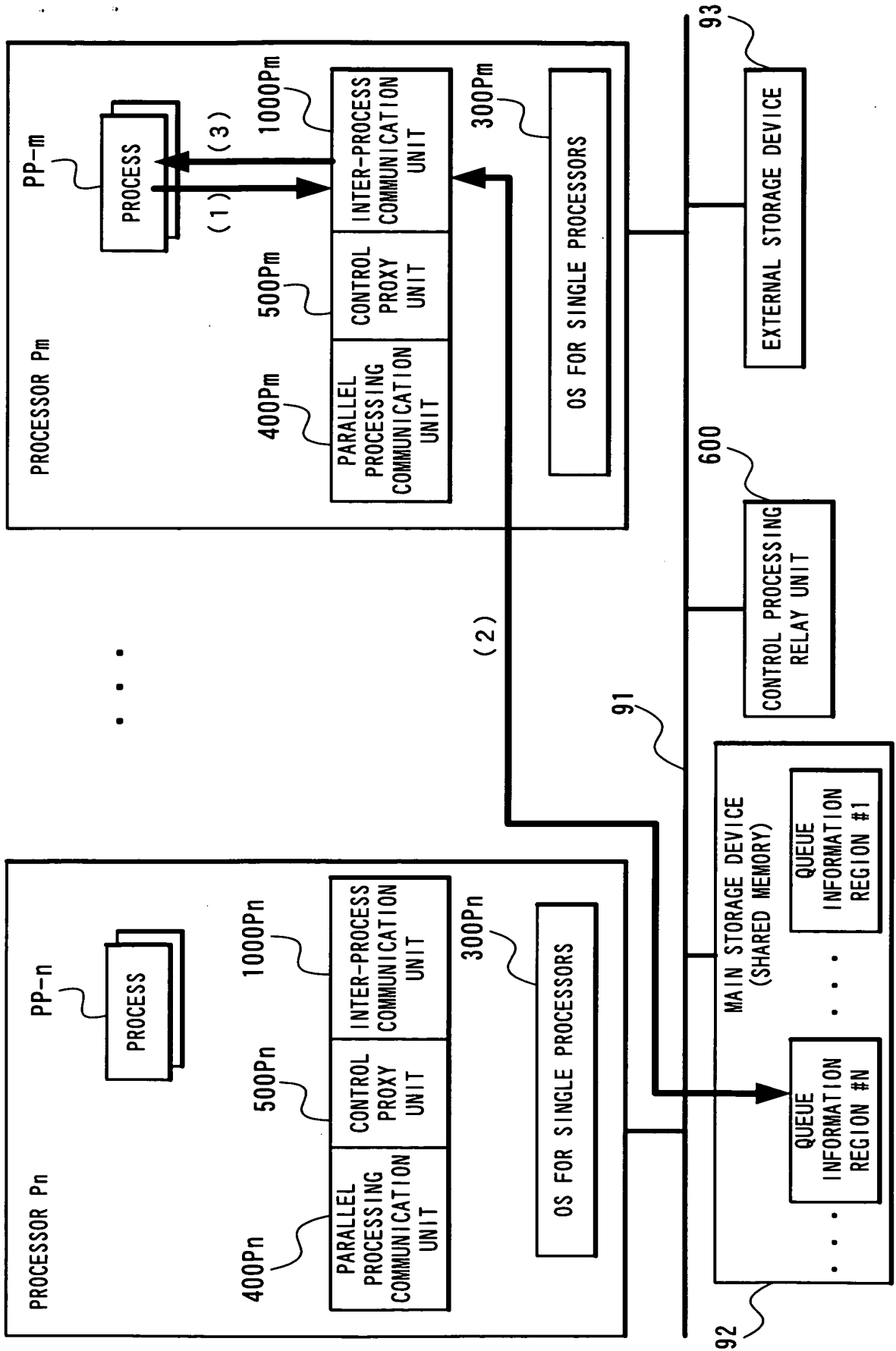


FIG. 23 (MESSAGE RECEPTION OPERATION BY MESSAGE QUEUE SYSTEM)

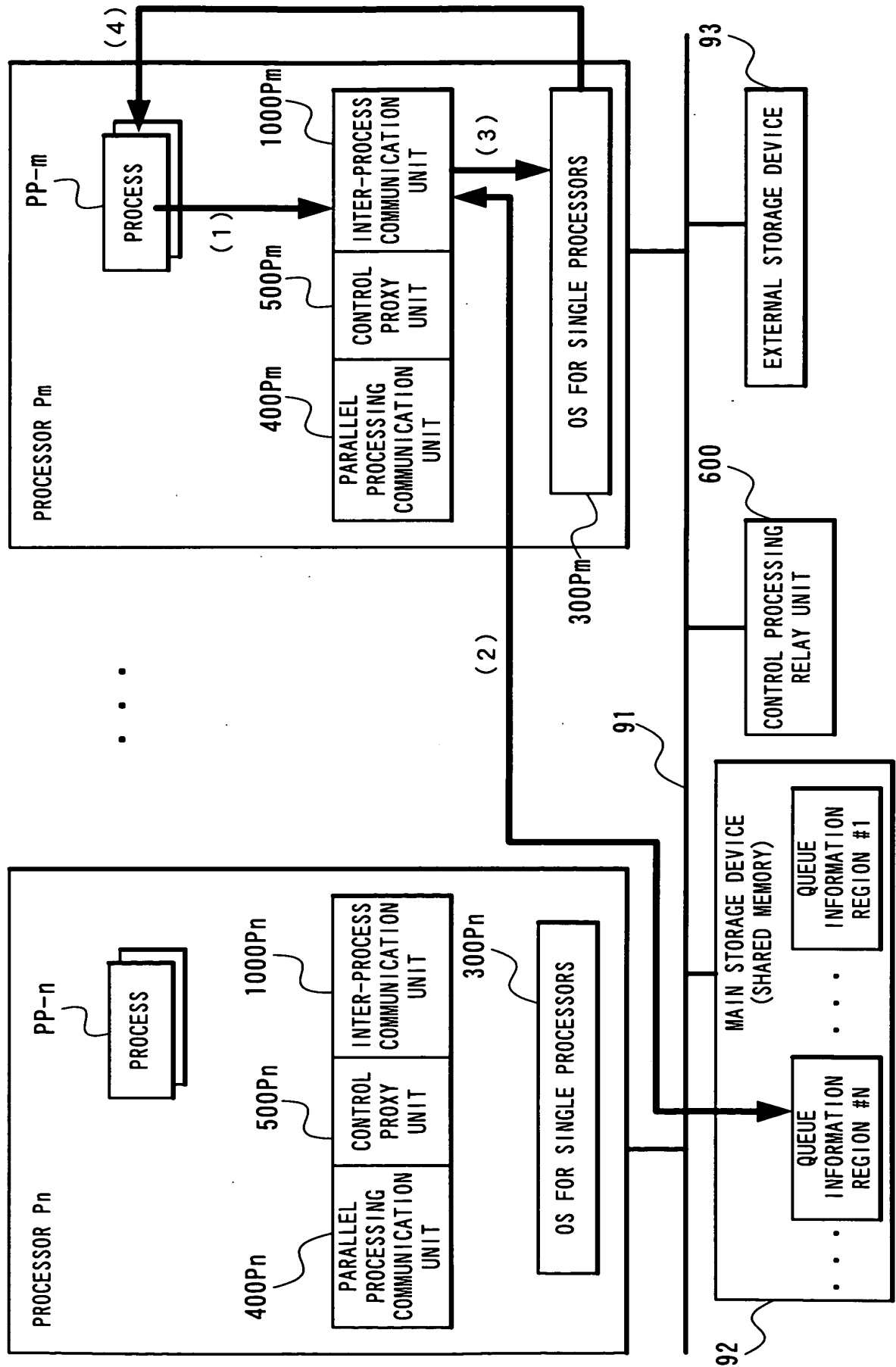


FIG. 24 (MESSAGE TRANSMISSION OPERATION BY MESSAGE QUEUE SYSTEM)

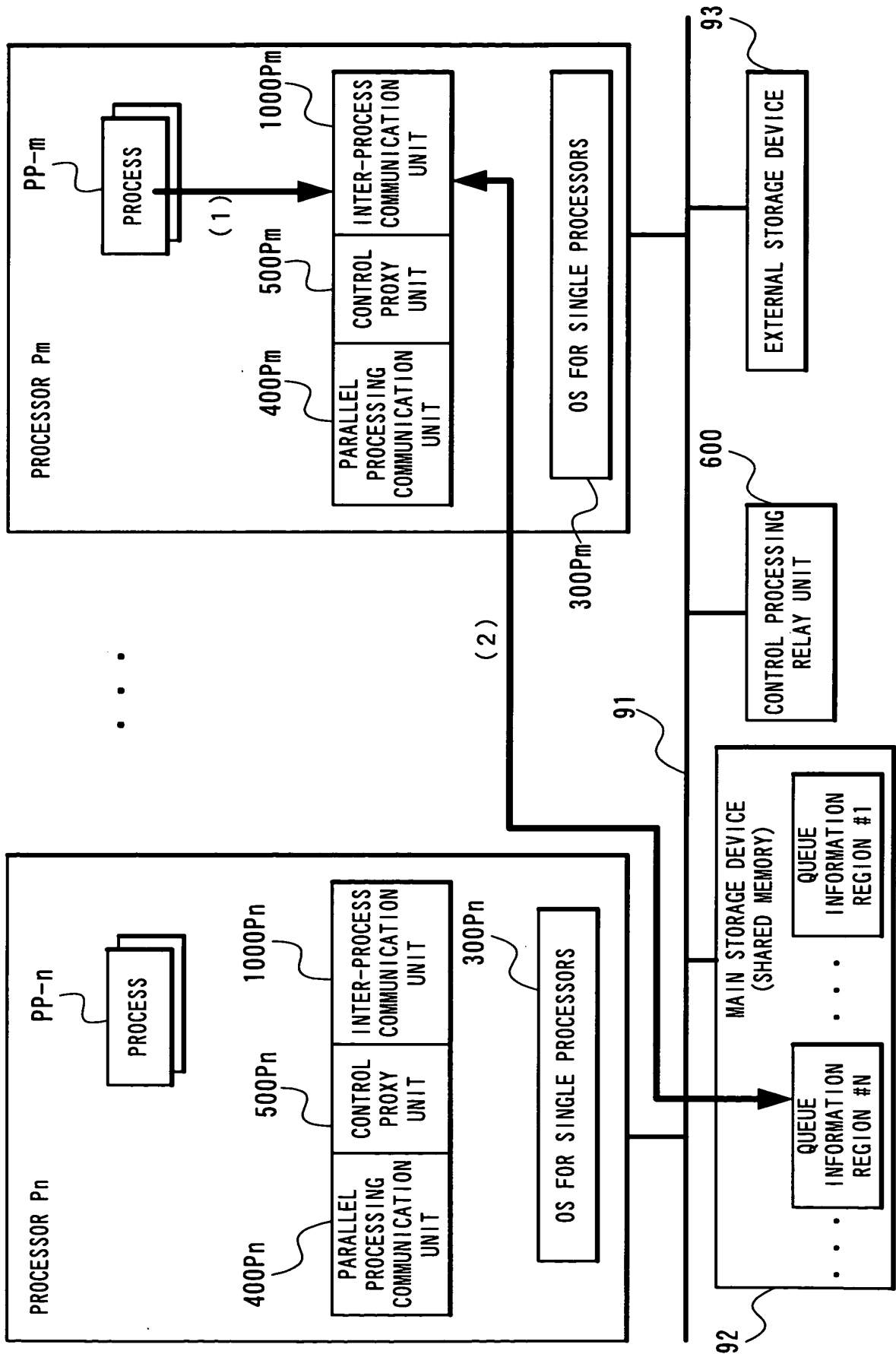


FIG. 25 (MESSAGE TRANSMISSION OPERATION BY MESSAGE QUEUE SYSTEM IN THE SAME PROCESSOR)

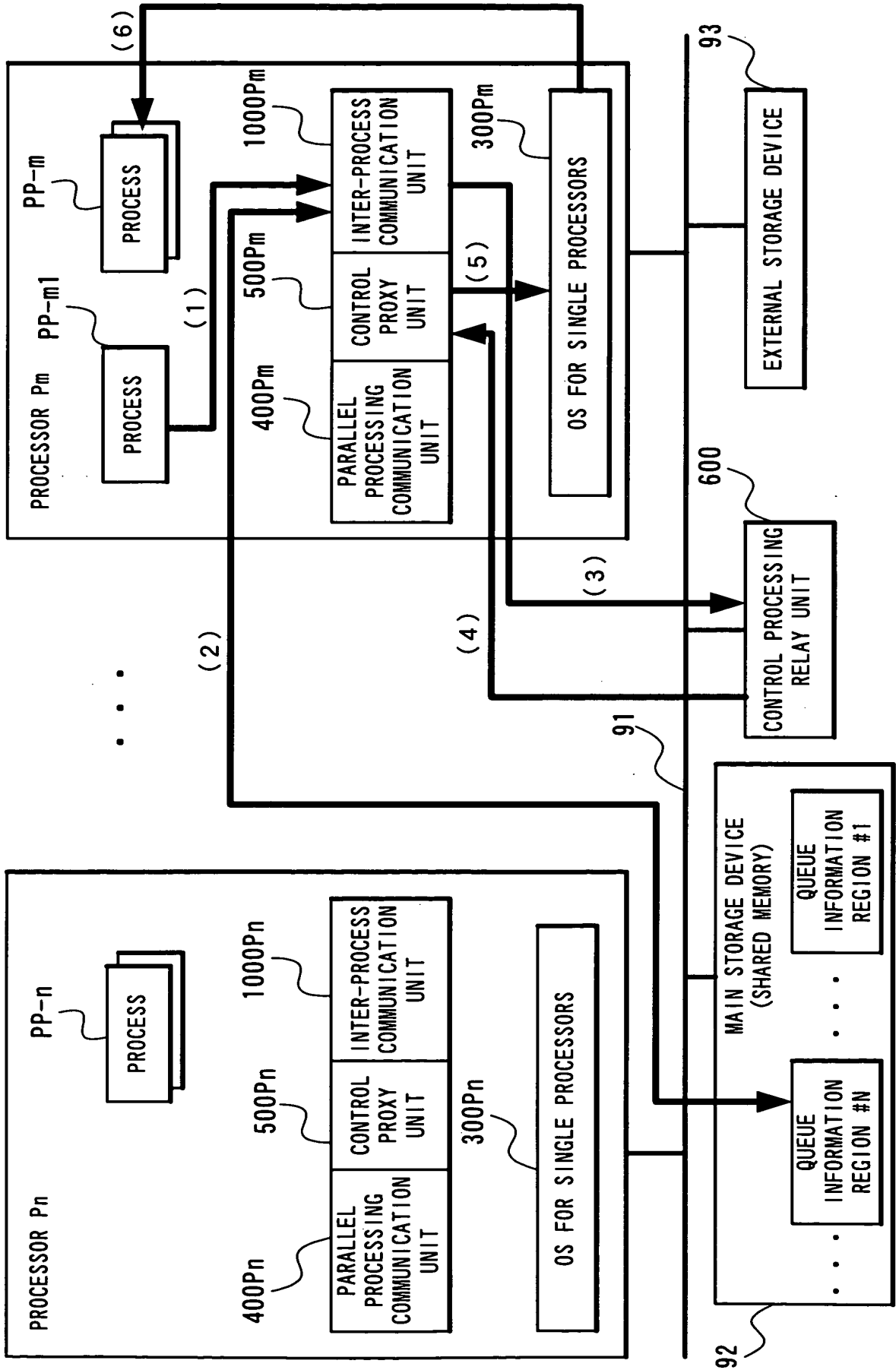


FIG. 26 (MESSAGE TRANSMISSION OPERATION BY MESSAGE QUEUE SYSTEM BETWEEN DIFFERENT PROCESSORS)

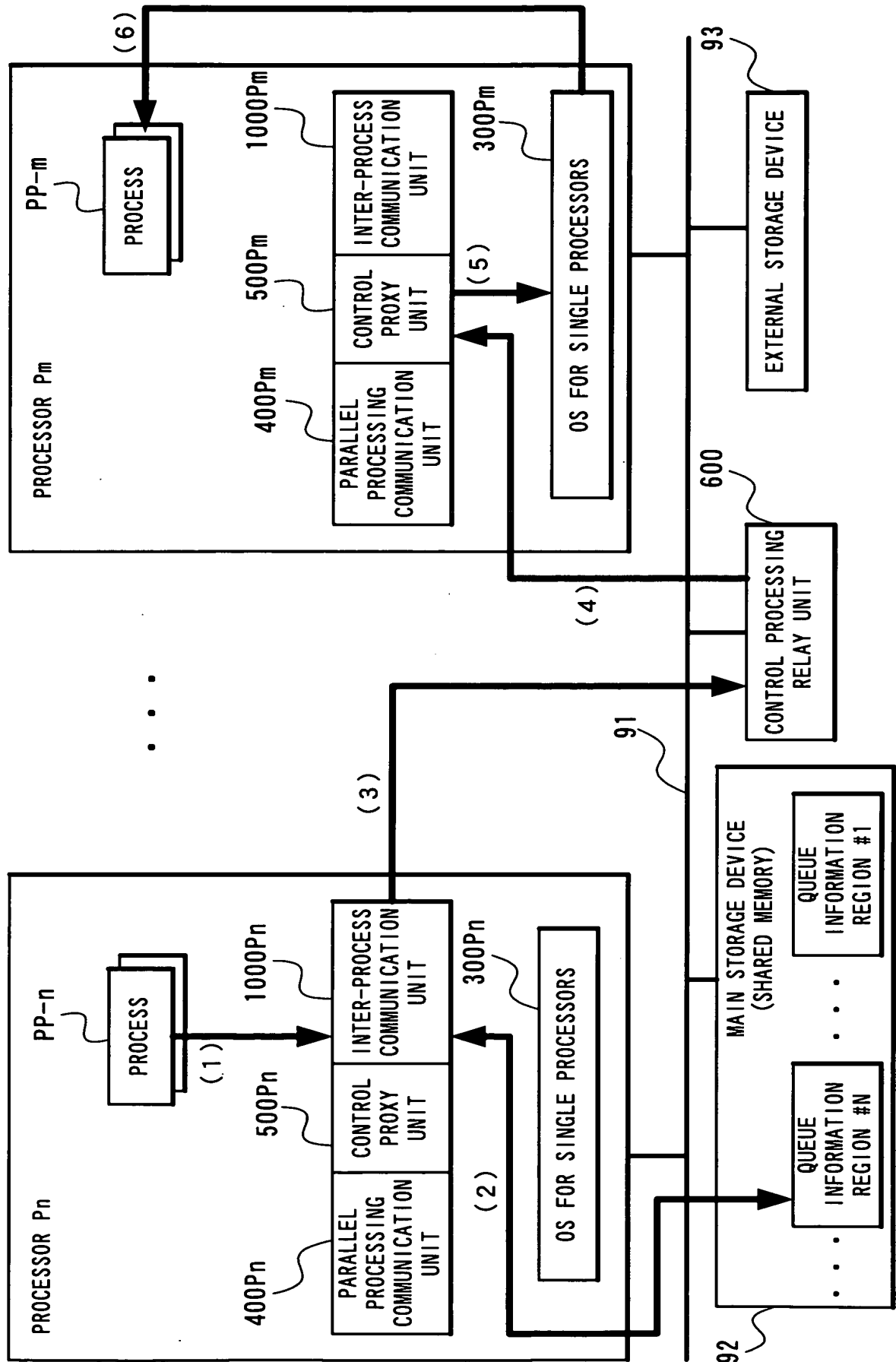


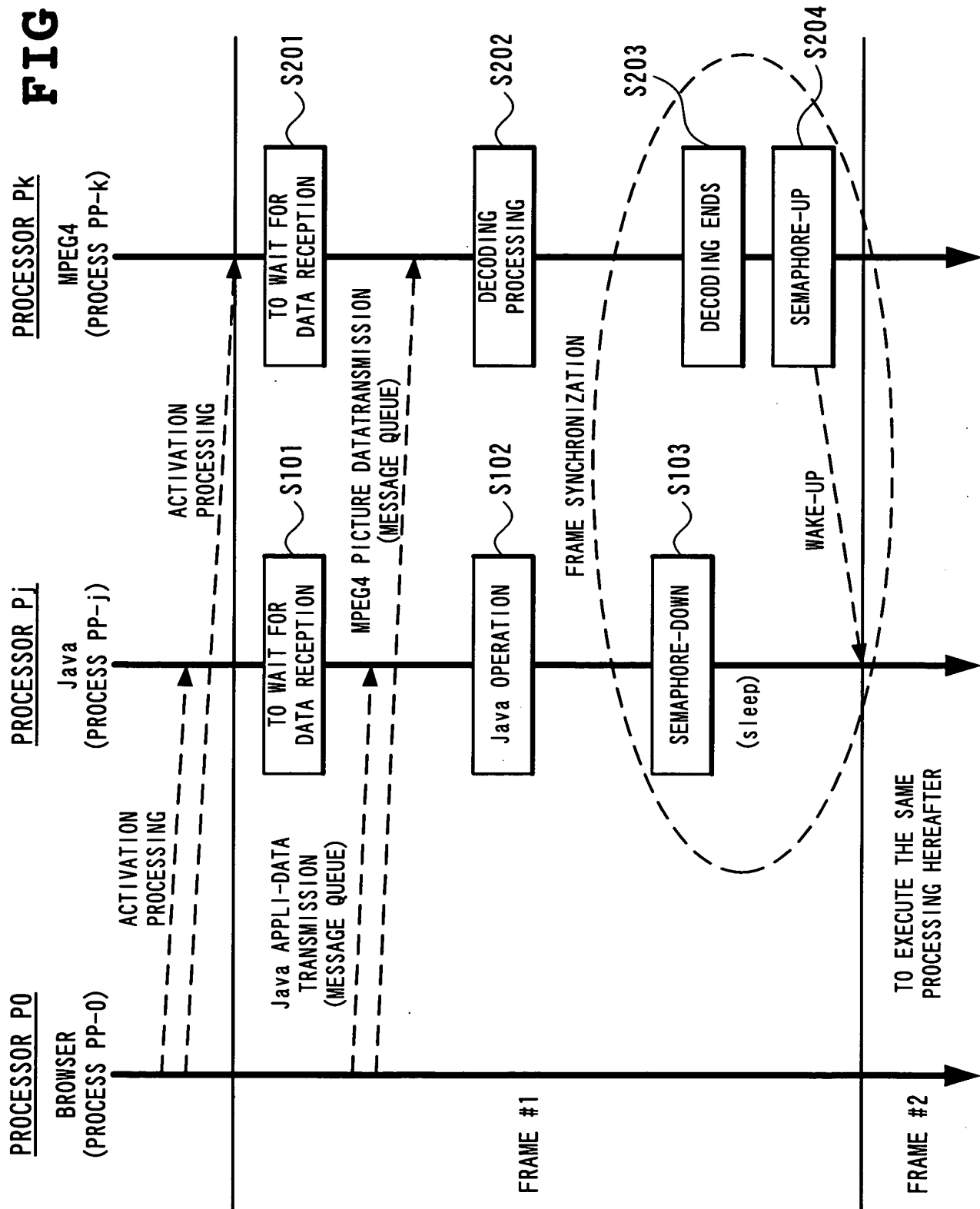
FIG. 27

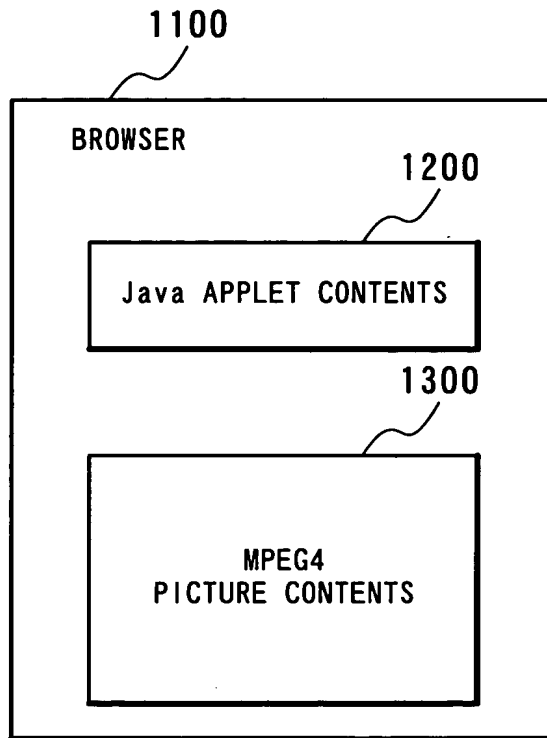
FIG. 28

FIG. 29